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## FOREWORD

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### Special Section of Papers Selected from 1995 Joint Technical Conference on Circuits/Systems, Computers and Communications (JTC-CSCC '95)



H. Yamada

In these years, Asian electronics industries have been growing very rapidly. Also, academic contributions from Asia have been remarkably increasing. The *Joint Technical Conference on Circuits/Systems, Computers and Communications*, known as "JTC-CSCC," was firstly established between Japan and Korea. Now the contributed papers are submitted from most of Asia. JTC-CSCC '95 was successfully held in Kumamoto during July 18-20, 1995, and they celebrated its tenth anniversary. Participants in the conference enjoyed very frank discussions and exchange of information.

After the conference, the editorial committee was formed and called for papers among those presented in the conference. Thirteen papers among twenty seven submitted papers were accepted for publication. It should be noted that the five papers are accepted from Asian countries, four from Korea, and one from India. This clearly shows the progress in Asian region.

As the guest editor, I would greatly appreciate their efforts of the editorial committee members of this special section. I would like to thank the reviewers for their efforts in spite of a limited time due to a tight publication deadline. Finally the editorial committee would like to express their deep thanks to all the authors who submitted papers to this Special Section.

The Editorial Committee of the Special Section:

Guest Editor	Hisashi Yamada	(TOSHIBA Corporation)
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	Shoichiro Yamasaki	(TOSHIBA Corporation)
	Hiroto Yasuura	(Kyushu University)

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Hisashi Yamada, Guest Editor

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**Hisashi Yamada** (*Member*) was born on March 13 in 1943. He received B.E. and M.E. degrees in communication engineering from Tohoku University, Sendai, Japan, in 1966 and 1968 respectively. Since 1968, he has been with Toshiba Corporation and has been developing analog LSIs, circuit simulation systems, MOS memories, micro computers, digital audio tape recorder systems, HDD and optical disk storage systems. During 1976-1978, he studied in Stanford University and received the engineer's degree in E.E. in 1978. He has been interested in analog LSIs, design CAD and application systems. From 1990 to 1994, he served as an associate editor of the IEICE Transactions on Electronics. He is now an associate editor of the International Journal of Analog Integrated Circuits and Signal Processing, Kluwer Academic Publishers. He is now a technology executive of DVD Business Division, Storage Media Business Group, Toshiba Corporation. He is a member of the Institute of Electrical Engineers of Japan, and the Institute of Television Engineers.

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