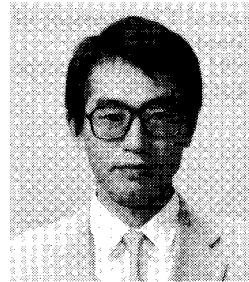

FOREWORD

Special Section of Selected Papers from the 9th Karuizawa Workshop on Circuits and Systems


H. Kikuchi



M. Sengoku

The ninth Karuizawa Workshop on Circuits and Systems (KWS) was held successfully during April 22nd to 23rd, 1996. The workshop ran in five parallel sessions consisted of 64 selected contributions, three special/panel sessions including 10 invited talks, additional four invited talks and a special panel discussion, which were focused both on fundamental and application aspects of hot topics in broader circuits and systems including nonlinear problems, signal processing, VLSI technology and concurrent systems. The KWS has been organized and run by the workshop committee of 67 young and active researchers and engineers. The success of the 9th KWS was totally realized by their contribution.

After the workshop, the editorial committee called for papers among those presented at the workshop. Nineteen papers out of 30 submitted papers were accepted for publication.

Among the young speakers of the workshop papers, the following four were selected as winners of the Young Excellent Author Award by the workshop committee. One of them is also included in this special section.

Takayuki Ohshima, Gunma University
 Kawori Takakubo, Tokyo Institute of Technology
 Taku Uchino, TOSHIBA CORPORATION
 Hiroyoshi Miwa, NTT

As guest editors, we would greatly appreciate their effort of the editorial committee members of this special section. We would also like to thank the reviewers for their cooperation in spite of a limited time due to the tight schedule for the special section. Finally we would like to express our deep thanks to all the authors who submitted papers to this special section.

The editorial committee members of this special section are:

Jinhui Chao	(Chuo University)
Toshihiro Hattori	(Hitachi, Ltd.)
Kunihiko Hiraishi	(Japan Advanced Institute of Science and Technology)
Yoshihiro Kaneko	(Gifu University)
Shinji Kimura	(Nara Institute of Science and Technology)
Mitsunori Makino	(Chuo University)
Hikaru Mizutani	(Shonan Institute of Technology)
Hideaki Okazaki	(Gifu National College of Technology)
Akira Onozawa	(NTT)
Toshimichi Saito	(Hosei University)
Akihiko Sugiyama	(NEC)

Masakiyo Suzuki	(Hokkaido University)
Mizuki Takahashi	(Sharp Corporation)
Hiroshi Tamura	(Niigata Institute of Technology)
Yoshihiro Ueda	(Oki Electric Industry Co., Ltd.)
Kiyotaka Yamamura	(Gunma University)
Michio Yotsuyanagi	(NEC)

Hisakazu Kikuchi, Guest Editor
General Chairperson of the 9th Karuizawa Workshop on Circuits and Systems

Masakazu Sengoku, Guest Editor
1995 General Chairperson of the Technical Group on Circuits and Systems,
IEICE

Hisakazu Kikuchi (*Member*) received B.E. and M.E. degrees in electronic engineering from Niigata University, Niigata, in 1974 and 1976, respectively, and Dr. Eng. degree in electrical and electronic engineering from Tokyo Institute of Technology, Tokyo, in 1988. From 1976 to 1979 he worked at System Laboratories, Fujitsu Ltd. Since 1979 he has been with Niigata University, where he is now Associate Professor in electrical engineering. During a year of 1992 to 1993, he was a visiting scientist at the Electrical Engineering Department, UCLA, Los Angeles, sponsored by the Ministry of Education, Science and Culture of Japan. His research interests include digital signal processing, image processing, wavelets, lightwave signal processing and mobile telecommunication. Dr. Kikuchi is a member of the IEEE and the Japan Society of Industrial and Applied Mathematics.

Masakazu Sengoku (*Member*) received the B.E. degree in electrical engineering from Niigata University, Niigata, Japan, 1967 and the M.E. and Ph.D. degrees from Hokkaido University in 1969 and 1972, respectively. In 1972, he joined the staff at Department of Electronic Engineering, Hokkaido University as a Research Associate. In 1978, he was an Associate Professor at Department of Information Engineering, Niigata University, where he is presently a Professor. His research interests include network theory, graph theory, transmission of information and mobile communications. He received the Paper Awards from IEICE in 1992 and 1996, and IEEE ICNNSP Best Paper Award in 1996. He was 1995 Chairman of IEICE Technical Group on Circuits and Systems (TG-CAS). He is a senior member of IEEE, a member of the Information Processing Society of Japan and Japan Society for Industrial and Applied Mathematics.
